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the actual current  $I_m$  can be corrected as follows when the coil is next activated in the then-following cycle:  $I_{corr} = I_m - I_o$

Please replace the consecutive paragraphs beginning at line 19 of page 8 with the following rewritten paragraphs:

A10 This weighted average value is one possible form of low-pass filter; others are conceivable as readily understood by the skilled artisan. In this case,  $I_{o,i}$  is the  $i$ th measurement of the offset error,  $I_m$  is the actual value of the current (raw value of the analog/digital converter 34) and  $k$  is a weighting factor.

The low-pass filtering takes account of the realization that the offset error  $I_o$  fluctuates in a temperature-dependent manner and changes slowly with respect to the sampling rate with which the offset error is determined.

#### In the Claims:

##### What is claimed is:

A13 1. (Amended) A method for determining the offset error of a measurement, where the measurement is subject to such an offset error of a coil current of an electromagnetic actuator, comprising:

measuring the coil current through a corresponding coil when the actuator is in a final position in which the coil is not supplied with current during the operation of the actuator; and providing the value obtained as the offset error.

2. (Amended) The method as claimed in claim 1, wherein the coil current is measured by potential tapping before and after a resistor connected in series with the coil, wherein the potential taps are being fed to a differential amplifier, and a constant value is added to a value output by the differential amplifier.

3. (Amended) The method as claimed in claim 1, wherein the actuator has two coils respectively assigned to the final position, and the coil current through the coil not assigned to the present final position is measured to determine the offset error.

5. (Amended) A circuit for determining the offset error of a measurement, the measurement subject to an offset error of a coil current I of an electromagnetic actuator, the circuit comprising:

a control circuit which evaluates the output of the differential amplifier when the coil is

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7. (Amended) The circuit as claimed in claim 5, wherein the actuator has first and second coils assigned to a final position, and

8. (Amended) The circuit as claimed in claim 7, wherein the control circuit for supplying current to the first and second coils transfers the actuator into a final position, and

the first coil assigned to the final position carries a capture current and a holding current, and the control circuit evaluates the output of the differential amplifier of the second coil.

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### **In the Abstract:**

Please replace the Abstract with the substitute Abstract attached hereto.